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FORM: PTO		U.S. DEPARTMENT OF COMMERCE				Atty Docket No:		Serial No:			
(REV: 7-80)		PATENT AND TRADEMARK OFFICE				95-0653		08/650,719			
						Applicant					
INFOR	MATIO	N DISCLOSU	RE STATEM	TENT BY APPL	ICANT	Mailloux					
						Filing Date:		Group:			
(37 CFR 1.9	(37 CFR 1.98(b))		(use several sheets if necessary)				5/20/96		2751		
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	AA	5,652,724	7/97	Manning			365	189.0	CE	VEI	
	AB	5,457,659	10/95	Schaefer			365	222			
	AC	5,452,261	9/95	Chung et al.			265	233 MA	R 02	1999	
	AD	5,280,594	1/94	Young et al.			395911	120 1			
	AE	5,058,066	10/91	Yu			/ 365	189.05 230.02 Gr	- I	2700	
	AP	4,870,622	9/89	Aria et al.			365	230.02	oup	2109	
	AG	5,668,773	09/97	Zagar et al.			365	233			
	AH	5,325,502	06/94	McLaury		/	395 111	A23 169			
	AI	5,373,227	12/94	Keeth			323	313			
	AJ	5,410,670	04/95	Hansen et al.			395 711	425 169		, , , , , , , , , , , , , , , , , , ,	
	AK	5,349,566	09/94	Merritt et al.			365	233.5			
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	Micron Technology, Inc., "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95										
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	"Possini Pantium PCLISA Chin Sat" Sumbanu I shamtarian antim Nah										
,	"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.										
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/	AT PCT Patent Application No. PCT/US95/16984, filed 12/22/1995, entitled: Burst Edo Memory Device									Device	
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FORM: PTC	D-1449	U	U.S. DEPARTMENT OF COMMERCE			Atty Docket No:		Serial No.		
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	AA	5,682,354	10/97	Manning		RADEMARY	365	233.5		
	AB	5,640,364	06/97	Merritt et al.			303	233.5		
	AC	5,729,504	12/95	Cowles	/		365	236		
	AD	5,661,695	08/97	Zagar et al.	/_		365	233.5	DEPEN-	
	AE	5,305,284	04/94	Iwase			365	238.5	JECEIVE	
	AF	5,325,330	06/94	Morgan	/		365	189.05	440	
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